



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of **Crockett**, et al : Date: January 22, 2002
Serial Number: 09/775,250 : Group Art Unit: 2841
Filed: February 01, 2001 : Examiner: I. Patel.
Title: **Insertion of Electrical Component within a Via of a Printed Circuit Board** : IBM CORPORATION
Intellectual Property Law
Department 9CCA/Building 002/2
P.O. Box 12195
Research Triangle Park, NC 27709

#7/Election
Hawkins
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RESPONSE AND ELECTION UNDER 37 CFR §1.111

Commissioner for Patents and Trademarks
Washington, D. C. 20231

Dear Sir:

In response to the Office Action dated **November 30, 2001**, please
amend the above-identified application as follows:

Certificate of Mailing § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope with sufficient postage addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on January 22, 2002.

Amirah Scarborough
Person mailing document

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09/775250

PRELIMINARY REMARKS

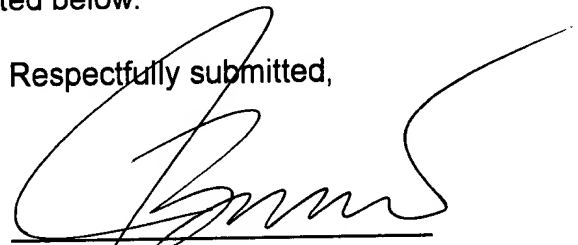
Claims 1-40 are pending in the present application. In the Office Action, the Examiner has restricted pending Claims per 35 U.S.C. §121 to one of two asserted invention categories including: (Group I) Claims 1-20, and (Group II) Claims 21-40.

REMARKS

Applicant has reviewed Examiner's comments. Although Applicant does not necessarily agree with Examiner's requirement for restriction, Applicant has nonetheless made a restriction election. Applicant hereby elects Group I Claims of Claims 1-20, without prejudice, per 37 C.F.R. §1.142(b). Applicant requests removal of the Examiner's restriction requirement. Applicant further asserts that the elected claims of the present application are allowable and requests early and favorable action from Examiner. The Examiner is invited to contact the undersigned for all issues of this Application at the telephone number and/or email address indicated below.

Respectfully submitted,

By:



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Dated: 1-22-02